

Shri Sant Gajanan Maharaj College of Engineering, Shegaon

Date: 12/08/2025

This is notified to all concerned that the **Pre-Defence of Mr. Mukesh Ravindra Chavan** for the award of Ph.D. in the subject **Electrical Engineering** in the faculty of **Science & Technology**, as per the provision of para-B-5.1 of SGBAU ordinance No.1 of 2016 is scheduled as mentioned below. His research topic is **“INDUCTION MOTOR FAULT DIAGNOSIS USING SIGNAL PROCESSING AND SOFT COMPUTING TECHNIQUES”**.

Date: 23rd August 2025

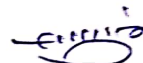
Time: 9.00 AM

Venue: Conference Room, Department of Electrical Engineering, SSGMCE, Shegaon

All interested faculty members, research scholars and PG students are invited to attend the same.



Dr. S. S. Jadhao
Chief Coordinator (R and D Cell)



Dr. S. B. Somani
Principal and Chairperson of RAC

Copy to:

- 1) Dr. Mrs. A.U. Jawadekar, Supervisor of Mr. Mukesh Ravindra Chavan
- 2) Dr. S. R. Paraskar, Member RAC and Subject Expert
- 3) Head, Electrical Engineering. Department for information and circulation amongst faculties and Ph.D. Scholars
- 4) All HODs for information and circulation amongst faculties and Ph.D. Scholars
- 5) Mr. A. B. Wagh for publication on website